Study on Performance of D-STATCOM Based Power Quality Enhancement in Distribution System

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ABSTRACT: Power quality is an important issue that is becoming increasingly important to electricity consumers at all levels of usage. It is a measure to provide a system supports for reliable operation for various loading condition. The intermittent switching of heavy nonlinear loads results in voltage sag, harmonics in distribution line and frequency variation. D-STATCOM is used to improve power quality of distribution system. Unit Template Algorithm is used for reference current generation which is a simple method for implementation and control. In this algorithm the control strategy, formations of unit vectors for inphase component and quadrature component is calculated for instantaneous value of voltage measurement at Pcc. The line parameter of 415 volt connected with linear loads are considered for analysis. The DSTATCOM model is done with MATLAB-SIMULATION software. Power quality improvement is analysed by comparing using controller and Unit Template algorithm with model without DSTATCOM.

KEYWORDS: Power Quality, D-STATCOM, Distribution Line, Unit Template Algorithm.

INTRODUCTION

Power Quality is either voltage quality or current quality which is maintained sinusoidal within tolerance range. The growing complexity of the modern power systems is largely due to the day-to-day changes in the system configuration to meet the increasing demand of electrical energy, through the process of installation of new generating station, interconnection of the transmission station, extra high voltage tie line etc. Some important issues for consideration in modern power system are

- Reactive power compensation
- Problems regarding stability of voltage (voltage fluctuations) and
- Harmonic effects due to sudden disturbances in the load.

Improved infrastructure is required for the existing power systems to facilitate effective utilization, superior control mechanism, reliability in operation and commercial profit. FACTS controllers are being extensively employed using advanced technology devices for effective utilization of the existing power system and for increasing power system’s dynamic performance and stability. Here application of D-STATCOM for improvement in power quality is discussed.

Two identified major power quality problems causing concern to customers are harmonics and voltage sags. Voltage sags is now focal issue, since the loads are voltage sensitive loads. It is the short duration voltage reduction due to load variation. The power industry can handle these challenges with the power electronics based technology of Flexible AC Transmission Systems (FACTS). FACTs have been defined by the IEEE as “Power electronics based system and other static equipment that provide control of one or more AC transmission system parameters to enhance controllability and increase power transfer capability”. Among the FACTs controllers, the shunt controllers have shown feasibility in terms of their cost effectiveness to solve a wide range of transmission to distribution level problems. The
distribution STATCOM (DSTATCOM) is similar to a STATCOM of a transmission system that uses a VSC of the required rating. However, the VSC used in a DSTATCOM is a six-pulse converter with SPWM or space vector modulated PWM (SVPWM) control over the magnitude of the injected AC voltage while maintaining a constant DC voltage across the capacitor. In the DSTATCOM, faster power semiconductor devices such as the IGBTs or IGCTs are used instead of the GTOs as in the STATCOM. The rapid switching capability provided by the IGBT (or IGCT) switches enables the use of DSTATCOM for balancing, active filtering and flicker mitigation. The unbalanced system is balanced by injecting a negative sequence current to the system. A DSTATCOM can be viewed as a controlled variable current source. The new voltage source converter topology is proposed for the DSTATCOM application. All the above problems can be solved by installing a DSTATCOM with proper controllers. In this paper DSTATCOM with Unit Template algorithm is considered as a controller to improve the power quality problem by reducing voltage sag.

II.D-STATCOM

It is called distribution static synchronous compensator. It is a shunt connected device. It consists of a VSI and a DC storage capacitor for realization. DSTATCOMs are basically categorized into three types, namely, single-phase two-wire, three-phase three-wire, and three-phase four-wire configurations, to meet the requirements of three types of consumer loads on supply systems. A major amount of AC power is consumed by three-phase loads. A substantial work has been reported on three-phase three-wire DSTATCOMs.

III.CONTROL STRATEGY

The main objective of a control algorithm of DSTATCOMs is to estimate the reference currents using feedback signals. These reference currents along with corresponding sensed currents are used in PWM current controllers to derive PWM gating signals for switching devices (IGBTs) of the VSC used as a DSTATCOM. Reference currents for the control of DSTATCOMs have to be derived accordingly and these signals may be estimated using a number of control algorithms. Here we are using Unit Template algorithm with hysteresis controller to get reference current.

Fig-1 Schematic diagram of DSTATCOM

It has a self-supporting DC voltage bus with a large DC capacitor. It is more widely used because it is light, cheap, and expandable to multilevel and multistep versions, to enhance the performance with lower switching frequencies.

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IV. UNIT TEMPLATE ALGORITHM

Calculation of source voltage amplitude

\[ V_t = \sqrt{\left( \frac{2}{3} \right) (V_a^2 + V_b^2 + V_c^2)} \]  

(1)

Calculation of in-phase template

\[ U_a = \frac{V_a}{V_t}; \ U_b = \frac{V_b}{V_t}; \ U_c = \frac{V_c}{V_t} \]  

(2)

Calculation of quadrature unit template

\[ W_a = -\frac{V_b}{\sqrt{3}}; \ W_b = \frac{\sqrt{3}}{2} U_a + \frac{V_b - V_c}{\sqrt{3}}; \ W_c = -\frac{\sqrt{3}}{2} U_a + \frac{V_b - V_c}{\sqrt{3}} \]  

(3)

Instantaneous active and reactive powers of the loads are,

\[ P_L = V_i(u_{aIA} + u_{bIB} + u_{cIC}); \ Q_L = V_i(w_{aIA} + w_{bIB} + w_{cIC}) \]  

(4)

The instantaneous load power is composed of two components as DC component and AC component. DC component is to be filtered out using low pass filter.

\[ P_L = \bar{p} + \bar{p}; \ Q_L = \bar{q} + \bar{q} \]  

(5)

For the computation of in-phase component of reference source current

Active power component of the source current \( I_{sm}^* \) and \( I_{sm}^* \) can be expressed as,

\[ I_{sm}^* = \left( \frac{2}{3} \right) \frac{\bar{p}}{V_t} \]  

(6)

\[ I_{sm}^* = K_{pd}V_{dce} + K_{id} \int V_{dce} dt \]  

(7)

Amplitude of active power component of the reference source current is given as,

\[ I_{sm}^* = I_{smp}^* + I_{sm}^* \]  

(8)

So for in-phase component of reference current we have to multiply \( I_{sm}^* \) with in-phase template. \( I_{sm}^* \) can be expressed as,

\[ I_{sad}^* = I_{sm}^* u_{aA}; \ I_{sbd}^* = I_{sm}^* u_{bB}; \ I_{scd}^* = I_{sm}^* u_{cC} \]  

(9)

B. For the computation of quadrature component of reference source current

Reactive power component of the source current \( I_{snq}^* \) and \( I_{sna}^* \) can be expressed as,

\[ I_{snq}^* = \left( \frac{2}{3} \right) \frac{\bar{q}}{V_t} \]  

(10)

\[ I_{sna}^* = K_{pd}V_{dce} + K_{id} \int V_{dce} dt \]  

(11)

Amplitude of reactive power component of the reference source current is given as,

\[ I_{sn}^* = I_{sna}^* - I_{snq}^* \]  

(12)

So for quadrature-phase component of reference source current we have to multiply \( I_{sn}^* \) with quadrature-phase template. \( I_{sn}^* \) can be expressed as,

\[ I_{sa}^* = I_{sn}^* w_{aA}; \ I_{sb}^* = I_{sn}^* w_{bB}; \ I_{sc}^* = I_{sn}^* w_{cC} \]  

(13)

C. For calculation of total reference source current

\[ I_{sa}^* = I_{sa}^* + I_{sad}^* \]  

(14)
The reference source current \( I_{sa} \), \( I_{sb} \), and \( I_{sc} \) is generated by summing the respective in-phase and the quadrature components of source current as shown in (8).

**Hysteresis Current Control**

To generate switching pulse for IGBT based VSC the actual source current is compared with reference source current in the hysteresis current controller. Carrier-less PWM hysteresis current controller is used for generating firing pulse of the gate drive of the IGBTs. The current errors are computed as, shown in (15).

\[ I_{saerr} = I_{sa}^* - I_{sa}; \]
\[ I_{sberr} = I_{sb}^* - I_{sb}; \]
\[ I_{scerr} = I_{sc}^* - I_{sc}; \] (15)

**V. SIMULATION AND RESULTS**

![Fig 2. Block diagram of D-STSTCOM with unit template algorithm](image-url)
Fig 3. Simulation model of DSTATCOM

Fig 4. Load side voltage and current with using DSTATCOM with Unit Template algorithm
VI. CONCLUSION

The dynamic performance of the DSTATCOM under linear lagging power factor unbalanced load condition is. The STATCOM is switched on at .05 s. At 1 s, the load is changed to a two-phase load. The PCC voltages (Vₕ), source currents (iₙ), load currents (iₗ), DC bus voltage (Vₑₑ), and amplitude of voltage (Vs) at PCC are depicted in this figure. It is also observed that the DC bus voltage of the DSTATCOM is maintained close to the reference value under all disturbances. The amplitude of the PCC voltage is maintained at the reference value under various load disturbances, which shows the PFC mode of operation of the DSTATCOM. During .5-1s load is unbalanced but grid current is balanced. DSTATCOM provides extra reactive power during unbalance condition.

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