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An Effective Color Interpolation in CFA Images Using Adaptive Enhancement Technique

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ABSTRACT: Low cost color interpolation technique proposed for the VLSI implementation for real time applications. Image interpolation is method of forcing new pixel within the range of discrete data points. This is applied diverse areas ranging from computer graphics, editing, and image reconstruction to online image viewing. High performance systems along with low power consumption are mandatory in any systems to be efficient. This technique is less complex and requires reduced memory. The aliasing artifacts resulted by the interpolation is reduced by sharpening filter and clamp filters. This architecture is designed with a SPST (Spurious Power Suppression Technique) adder or unsigned adder which reduces the power consumption and increases speed by considerable amount.

KEYWORDS: interpolation, enhancement, SPST(spurious power suppression technique)

I.INTRODUCTION

Digital cameras are integrated into many consumer electronic products, such as digital camera, digital video, smart TV, smart phone, tablet PC, etc. The digital cameras are manufactured by a CCD or a CMOS image sensor that can capture images by color filter array (CFA) technique. The red (R), green (G), and blue (B) colors are sampled as one color in each pixel [1]. Fig. 1 shows a color filter arrays called Bayer CFA, in which two colors have disappeared in each pixel. Thus, it is important to reconstruct the images from CFA to full RGB formats.

${ m B}_{0,0}$	G _{0,1}	B _{0,2}	G _{0,3}	${ m B}_{0,4}$	G _{0,5}
G _{1,0}	R _{1,1}	G _{1,2}	R _{1,3}	G _{1,4}	R _{1,5}
B _{2,0}	G _{2,1}	B _{2,2}	G _{2,3}	B _{2,4}	G _{2,5}
G _{3,0}	R _{3,1}	G _{3,2}	R _{3,3}	G _{3,4}	R _{3,5}
B _{4,0}	G _{4,1}	B _{4,2}	G _{4,3}	B _{4,4}	G _{4,5}
G _{5,0}	R _{5,1}	G _{5,2}	R _{5,3}	G _{5,4}	R _{5,5}

Fig.1 Bayer Color Filter Array

II. THE PROPOSED ALGORITHM

The proposed novel color interpolation algorithm is composed of low-complexity edge detection, a green color interpolation, and red-blue color interpolation techniques. Each color is interpolated by different methodologies according to the relative locations and reference neighboring samples. In which the BRg(i, j) and RBg(i, j) represent that the green color pixel g(i, j) was interpolated and prepared when it interpolates R(i, j) and B(i, j). The details of each technique will be described in the following subsections.



(An ISO 3297: 2007 Certified Organization)

Vol. 4, Special Issue 6, May 2015

SPST :The SPST is a Single Pole Single Throw switch. It is used in electronic as well as electrical circuits.

A. Low-Complexity Edge Detection

In order to discover the edge information by low-complexity methodology, the difference in the vertical (DV) and horizontal (DH) directions were used. The DV neighboring around the pixel G(i, j) or RB(i, j) can be evaluated by

$$DVi_{i}=|RBi-1,j-1-RBi+1,j-1|+|Gi-1,j-Gi+1,j|+|RBi-1,j+1-RBi+1,j+1|$$
(1)

where G is the pixel in green color and RB is the pixel in red or blue color in the CFA images. The relative locations and reference neighboring RGB pixels .On the other hand, the difference in the horizontal direction (DH) neighboring around the pixel G(i, j) or RB(i, j) can be evaluated by

DHi,=|RBi+1,j+1-RBi+1,j-1|+|Gi,j+1-Gi,j-1| + |RBi-1,j-1| + |RBi-1,j-1|(2)

After obtaining the difference in the horizontal (DH) and vertical (DV) directions, the total difference (TD) neighboring around the pixel G(i, j) or RB(i, j) can be calculated by

(3)

TDi,=*DHi*,*j*+*DVi*,*j* where DH and DV are all positive value.

B. Green Color Interpolation

In order to improve the quality of interpolated images, an anisotropic weighting model was created for this design the

reference neighboring pixels in horizontal direction are much more than those in vertical direction. The green color pixels

can be interpolated by referring to 75 % weighting of pixels in the horizontal direction and 25 % weighting of pixels in the

vertical direction. In order to reduce the computing resource of the proposed color interpolation algorithm, all division operations were replaced by shift operations.

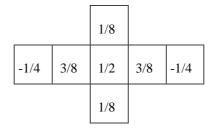
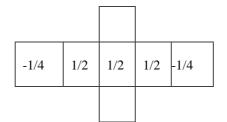


Fig. 2. The parameters of green color interpolation (a) without edge enhancement model

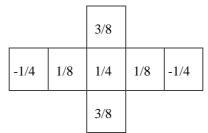


(b) with edge enhancement in horizontal direction model



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(c) with edge enhancement in vertical direction model.

To reconstruct the green color in CFA format image, there are two different causes for interpolating G(i, j). the values of TD, DH, and DV can be used to adaptively select one of the three green color interpolation models, without edge enhancement, edge enhancement in horizontal direction, and edge enhancement in vertical direction, as the interpolation model according to the edge information neighboring around the pixel G(i, j). If the value of the total difference (TD) is less than the threshold value, the value of G(RB)(i, j) can be calculated by the without edge enhancement model as

 $\begin{aligned} Gi, (RB) &= 38 \ (Gi, -1 + Gi, j + 1) + 18 \ (Gi - 1, j + Gi + 1, j) + RBi, j \\ &- 12 \ (12 \ (RBi, + RBi, -2) + 12 \ (RBi, j + RBi, j + 2)) \ (4) \end{aligned}$

Otherwise, if the value of the total difference (TD) is larger than the threshold value and DH is less than DV, the value of G(RB)(i, j) can be obtained by the edge enhancement in horizontal direction model as Gi, (RB) = 12 (Gi, -1 + Gi, j+1) + RBi, j - 12 (12 (RBi, j + RBi, j-2) + 12 (RBi, + RBi, +2))(5)

Also, if the value of total difference (TD) is larger than the threshold value and the value of DH is larger than DV, the value of G(RB)(i, j) can be computed by the edge enhancement in vertical direction model as $Gi, (RB) = 18 (Gi, -1 + Gi, j+1) + 38 (Gi-1, j + Gi+1, j) + 12 \{RBi,$

-12 (12 (RBi, +RBi,-2) + 12 (RBi, j + RBi, j+2)))(6)

By analyzing the parameters of these three green color interpolation models, it is obviously that the characteristic of the compensation for green color is a spatial sharpening filter. This spatial sharpening filter can efficiently reduce the blurring effect. Moreover, the edge information can be enhanced efficiently by the proposed adaptive edge enhancement technique

C.Red and Blue Colors Interpolation

To reconstruct the red and blue colors in CFA format image, it is important to use the information of the four neighboring green colors. The relative locations and reference neighboring samples of G(i, j+1), G(i-1, j), G(i, j-1), and G(i+1, j) where the pixel in CFA format is R(i, j) or B(i, j). To be able to enhance the edge information, the values of TD, DH, and DV can be used to adaptively select one of the three red and blue interpolation models, without edge enhancement, edge enhancement in horizontal direction, and edge enhancement in vertical direction, as the interpolation model according to the edge information neighboring around the pixel R(B) (i, j) or B(R) (i, j).

1/4	-1/4	1/4
-1/4	1	-1/4
1/4	-1/4	1/4

Fig. 3. The parameters of red and blue colors interpolation at G(i,j) (a) without edge enhancement model



(An ISO 3297: 2007 Certified Organization)

Vol. 4, Special Issue 6, May 2015

1/4	-3/8	1/4
-1/8	1	-1/8
1/4	-3/8	1/4

(b) with edge enhancement in horizontal direction model

1/4	-1/8	1/4
-3/8	1	-3/8
1/4	-1/8	1/4

(c) with edge enhancement in vertical direction model

If the value of total difference (TD) is less than a threshold value, then the value of R(B)(i, j) or B(R)(i, j) can be calculated by the without edge enhancement model as shown below

RBi, (BR) = 14 (RBi-1, -1 + RBi-1, j+1 + RBi+1, j-1 + RBi+1, j+1)+gi, -14 (Gi-1, + Gi+1, + Gi, -1 + Gi, j+1)(7)

where g(i, j) is created by green color interpolation which is mentioned above. Fig. 3 (a) shows the parameters of red and blue color interpolation without edge enhancement model. Otherwise, if the value of TD is larger than the threshold value and DH is less than DV, then the value of R(B)(i, j) or B(R)(i, j) can be obtained by the edge enhancement in horizontal direction model as

$$RBi,(BR)G = 14(RBi-1,j-1+RBi-1,j+1+RBi+1,j-1+RBi+1,j+1) +gi, -18 (3 * (Gi-1, +Gi+1,) + Gi,j-1+Gi,j+1) (8)$$

Also, if the value of TD is larger than the threshold value and DH is larger than DV, the value of R(B)(i, j) or B(R)(i, j) can be computed by the edge enhancement in vertical direction model as

$$RBi, (BR) = 14 (RBi-1, -1 + RBi-1, j+1 + RBi+1, j-1 + RBi+1, +1) + gi, j - 18 (Gi-1, j + Gi+1, j + 3 * (Gi, j-1 + Gi, +1))$$
(9)

By analyzing the parameters of these interpolation models having three R and B colors, it can be seen that the individualized of the compensation for R and B colors are Laplacian filters [9].

The red or blue color modernized pixel RB(i, j) where its upper and bottom pixels are RB(i-1, j) and RB(i+1, j) at G(i, j) can be calculated as

$$RBi$$
, $(G) = 12 (RBi-1, + RBi+1,) + 12 Gi, j - 18 (Gi-1, j-1)$

$$Gi-1,+1+Gi+1,j-1+Gi+1,j+1$$
 (10)

The blue or red color reconstructed pixel BR(i, j) where its right and left pixels are BR(i, j-1) and BR(i, j+1) at G(i, j) can be computed as shown below



(An ISO 3297: 2007 Certified Organization)

Vol. 4, Special Issue 6, May 2015

BRi, (G) = 12 (BRi, -1 + BRi, j+1) + G - 12 (gi, j-1 + gi, j+1)(11)

The parameters of the proposed G, R, and B interpolation equations are intended as 1/2, 1/4, 1/8, and 3/8. It provides a cost-efficient base for VLSI implementation by replacing the multipliers and dividers also with the shifters and adders.

III. VLSI ARCHITECTURE

In real-time compact disk applications, it is obligatory to develop low computational complexity, low-memory necessity, high performance, and high quality scaling algorithms. The proposed adaptive edge-enhanced scaling algorithm provides a favorable base for VLSI implementation. Fig.4 shows the block diagram of the architecture for the proposed adaptive edge-enhanced scalar. It consists of five main blocks: a register bank, a sharp filter, an edge detector, a simplified bilinear interpolation circuit, and a controller. The details of the register bank are present in Section II B and other parts will be described in the following subsections.

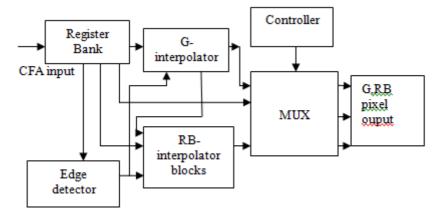


Fig 4 Block diagram of the VLSI architecture for the proposed color interpolation

A. Register Bank

The register bank was designed to real-time provide fifteen pixels in CFA format for processing the G interpolator and three RB interpolators during each cycle. It is intended with a two-line-buffer memory and constructed with fifteen shift registers. The projected register bank is designed such that only one value of pixel from memory is received in each cycle time, and then provides fifteen values of CFA pixels as inputs for color interpolation. By adding this register bank, the proposed color interpolation processor achieves the memory access throughout pixel in and pixel out.

B. Edge Detector

Fig. 8 shows the architecture of the proposed edge detector. It consists of six absolute sub tractors (|Sub|) and five adders (Add). The eight input signals take delivery of their inputs from the register bank. Behind being processed by the edge detector, the values of TD, DH, and DV at the position of P(i, j) are produced for the green color interpolator and the first model of red and blue color interpolator (RB_M1). In addition, the output signals of the edge detector are in addition sent to the controller. The output signal of TD provides information of the edge intensity. The further two output signals, DH and DV, give the direction in sequence of the edges.

C. Interpolation Error

This section briefly introduces the interpolation error theorem and linear interpolation error.



(An ISO 3297: 2007 Certified Organization)

Vol. 4, Special Issue 6, May 2015

D. Red and Blue Colors Interpolators

To analyze equations (7), (8), and (9), all input signals should be the same except the values of their parameters. Hence, the reconfigurable technique can be used to design the hardware structural design of the red and blue colors interpolator. It consists of nine adders, one subtractor, two multiplexers, and five shifters. This design can be reconfigured as the functions of Eq. (7), (8), or (9) according to the values of TD, DH, and DV during each processing cycle.

E. Controller

It provides control signals to the multiplexer for selecting input data for the interpolators and is capable of sending reconfigurable control signals for changing the architecture of the interpolators. Moreover, the controller must monitor its input and output data access with the memory to fit the performance of pixel-in and pixel-out. Finally, the proposed color interpolation processor achieves high performance and high throughput.

IV. SIMULATION RESULTS

The following figures shows simulated results of color interpolation

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Fig 6 RB-M2



(An ISO 3297: 2007 Certified Organization) Vol. 4, Special Issue 6, May 2015

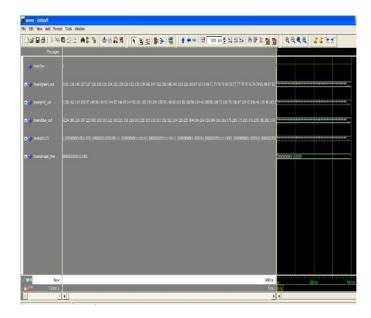


Fig 7 Main output

IV. CONCLUSION

A novel color interpolation algorithm is proposed to develop a low-cost, low-power, high performance, and high quality color interpolation for real-time video applications. A SPST switch model, an edge detector, clamp and sharpening filters have been used to reduce the power onsumption and improve the quality of the images.

Fig 5 RB-M3

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