

(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 4, April 2016

VLSI Design of Syndrome Computation Block for RS(255,239) Code

Pranali D. Surkar^{1,} Swapnil D. Ninawe²

Assistant Professor, Dept. of Electronics and Communication Engineering, PIET, Nagpur, Maharashtra, India¹

Assistant Professor, Dept. of Electronics and Communication Engineering, PIET, Nagpur, Maharashtra, India²

ABSTRACT: This paper presents a design of syndrome computation block for Reed-Solomon decoder for RS (255, 239) code. For calculating the syndromes, three-parallel processing has been used. Parallelizing allow inputs to be received at very high rates and outputs to be delivered at correspondingly high rates with minimum delay. RS (255, 239) code consists of 255 symbols which are multiple of 3. Hence three symbols are inputted parallely in syndrome computation block. Complete process required 85 clock cycles to compute one syndrome coefficient. The proposed three-parallel RS decoder architecture is modeled in Verilog HDL and simulated to verify its functionality. This process can be adapted in the FEC devices.

KEYWORDS: Galois Field, Reed-Solomon codes, syndrome, three-parallel.

I. INTRODUCTION

Reed-Solomon (RS) coding is one of the most powerful and standardized technique for error and erasure correction [1]. Owing to its excellent capability for correcting burst errors, these codes have been widely used in a variety of communication systems such as storage devices (CD, DVD, etc), space communication links, wireless system as well as networking communications. Adapting pipeline technique in the conventional syndrome block is crucial but due to its feedback loop, applying pipeline technique to the syndrome block will make its required clock cycles double [2]. For two parallel syndrome computation over GF (2^8) we have to add one dummy zero input where they need n/2 clock cycles to finish [3].

In this paper, we present the three-parallel syndrome computation block for RS decoder. For this, we consider RS (255, 239) code. Section II describes the Galois field arithmetic which includes Galois field addition and multiplication. And also shows the representation of field elements and its binary form. Section III shows the calculations of syndrome and in section IV, architecture of three-parallel syndrome computation (SC) block for RS decoder has discussed.

II. RELATED WORK

A Galois field (also called finite field) is a mathematical structure which plays a crucial role in the theory of RS codes and the finite field arithmetic operations are the fundamental building blocks for the RS decoder [5]. The Reed-Solomon code is defined in the Galois field which contains a finite set of numbers where any arithmetic operations on elements of that set will result in an element belonging to the same set [6]. Every element, except zero can be expressed as a power of a primitive element, α of the field. RS (255, 239) code belongs to GF (2⁸). Therefore the primitive polynomial over GF (2⁸) will be $x^8+x^4+x^3+x^2+1$. This is a polynomial of degree m which is irreducible that is, a polynomial with no factors. Table. 1 represents the field elements and its binary form over GF (2⁸).

A. Galois Field Adder (2^8)

An addition of two elements in the Galois field is simply the exclusive-OR (XOR) operation.



(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 4, April 2016

B. Galois Field Multiplier (2^8)

Multiplication is more complex than addition as the addition is only XOR operation. But multiplication is different and depends on Galois field structure and its primitive polynomial and the type of basis coefficients. Let A and B be the multiplier and multiplicand over GF (2^8) , respectively. A and B can be describe as equation 1.

Let A and B be the multiplier and multiplicand over OF (2), respectively. A and B can be describe as equation 1. $A = a_i + a_i \alpha_i + \dots + a_{m-1} \alpha_m^{m-1} = \sum_{i=1}^{m-1} a_i \alpha_i^i$ (1)

Field elements in powers of α		Field elements in binary form							
	α^0	α^1	α^2	α^3	α^4	α ⁵	α^{6}	α ⁷	
0	0	0	0	0	0	0	0	0	0
α^0	1	0	0	0	0	0	0	0	1
α^{1}	0	1	0	0	0	0	0	0	2
α^2	0	0	1	0	0	0	0	0	4
α^3	0	0	0	1	0	0	0	0	8
α^4	0	0	0	0	1	0	0	0	16
α ⁵	0	0	0	0	0	1	0	0	32
α^6	0	0	0	0	0	0	1	0	64
α7	0	0	0	0	0	0	0	1	128
α^8	1	0	1	1	1	0	0	0	29
α9	0	1	0	1	1	1	0	0	58
•	•	•	•	•	•		•	•	
•	•	•	•	•	•	•	•	•	
	•	•		•			•	•	
α^{254}	0	1	1	1	0	0	0	1	142

Table.1 Field elements and its binary form over $GF(2^8)$.

Then their product Z can be described as equation (3).

$$\mathbf{Z} = \mathbf{A} \cdot \mathbf{B} = \mathbf{A} \cdot \sum_{i=0}^{m-1} b_i \alpha^i = \sum_{i=0}^{m-1} b_i (\mathbf{A} \alpha^i)$$

(3)

The GF (2^8) multiplier is implemented as equation (4).

$$z_0 = a_0b_0 + a_7b_1 + a_6b_2 + a_5b_3 + a_4b_4 + (a_3 + a_7)b_5 + (a_2 + a_6 + a_7)b_6 + (a_1 + a_5 + a_6 + a_7)b_7$$

$$z_1 = a_1b_0 + a_0b_1 + a_7b_2 + a_6b_3 + a_5b_4 + a_4b_5 + (a_3 + a_7)b_6 + (a_2 + a_6 + a_7)b_7$$

$$z_2 = a_2b_0 + (a_1 + a_7)b_1 + (a_0 + a_6)b_2 + (a_5 + a_7)b_3 + (a_4 + a_6)b_4 + (a_3 + a_5 + a_7)b_5 + (a_2 + a_4 + a_6 + a_7)b_6 + (a_1 + a_3 + a_5 + a_6)b_7$$



(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 4, April 2016

- $z_3 = a_3b_0 + (a_2 + a_7)b_1 + (a_1 + a_6 + a_7)b_2 + (a_0 + a_5 + a_3)b_3 + (a_4 + a_5 + a_7)b_4 + (a_3 + a_4 + a_6 + a_7)b_5 + (a_2 + a_3 + a_5 + a_6)b_6 + (a_1 + a_2 + a_4 + a_5)b_7$
- $\begin{aligned} z_4 &= a_4 b_0 + (a_3 + a_7) b_1 + (a_2 + a_6 + a_7) b_2 + (a_1 + a_5 + a_6 + a_7) b_3 + (a_0 + a_4 + a_5 + a_6) b_4 \\ &+ (a_3 + a_4 + a_5) b_5 + (a_2 + a_3 + a_4) b_6 + (a_1 + a_2 + a_3 + a_7) b_7 \end{aligned}$ $\begin{aligned} z_5 &= a_5 b_0 + a_4 b_1 + (a_3 + a_7) b_2 + (a_2 + a_6 + a_7) b_3 + (a_1 + a_5 + a_6 + a_7) b_4 + (a_0 + a_4 + a_5 + a_6) b_5 + (a_3 + a_4 + a_5) b_6 + (a_2 + a_3 + a_4) b_7 \end{aligned}$
- $z_6 = a_6b_0 + a_5b_1 + a_4b_2 + (a_3 + a_7)b_3 + (a_2 + a_6 + a_7)b_4 + (a_1 + a_5 + a_6 + a_7)b_5 + (a_0 + a_4 + a_5 + a_6)b_6 + (a_3 + a_4 + a_5)b_7$
- $z_7 = a_7b_0 + a_6b_1 + a_5b_2 + a_4b_3 + (a_3 + a_7)b_4 + (a_2 + a_6 + a_7)b_5 + (a_1 + a_5 + a_6 + a_7)b_6 + (a_0 + a_4 + a_5 + a_6)b_7$

							1,000,000 ps
Name	Value	1999,995 p	ns 1999,996 ps	1999,997 ps	999,998 ps	1999,999 ps	1,000,000 ps
▶ 💐 z[7:0]	00100000			00100000			
▶ 💐 a[7:0]	00000100			00000100			
▶ 📑 b[7:0]	00001000			00001000	_		
	-						

Figure 1 : Simulation result of Galois field multiplier (2⁸)

Fig.1 shows the simulation result of Galois Field Multiplier (2^8) which gives the 8 bit output after giving 8 bit inputs.

III. SYNDROME CALCULATIONS

Syndrome is actually a set of symptoms or characteristics indicating the existence of undesirable condition or problem. The first step in RS decoder is to check if there is any error in the codeword or not. This is done using syndrome computation block by calculating syndromes coefficients. Here the syndrome is in the form of bit error. An (n, k) RS code has k message symbols and n coded symbols, where code symbol belongs to GF (2^{m}). The (n, k) RS code has a minimum distance of d. From an RS code, an error can be corrected by finding out the error location and error value, where an erasure is defined as an error with a known error location. RS code is specified as RS (n, k) with m-bit symbols. RS (n, k) codes on m-bit symbols exist for all n and k for which $0 < n < k \le 2^{m-1}$. For this project, (255, 239) RS code [1] is targeted for syndrome computation block over the Galois field (2^{8}). Therefore the primitive polynomial over GF (2^{8}) will be $x^{8}+x^{4}+x^{3}+x^{2}+1$. From the primitive polynomial, binary elements of the polynomial are obtained. For this, n = 255 is the total no. of code symbols after encoding called codeword.

k = 239 is the no. of data symbols to be encoded.

m = 8 is no. of bits

t = (n - k) / 2 is the no. of symbols that can be corrected with this code

n - k = 16 syndromes to calculate

Syndrome computation block calculates all the syndromes Si $(0 \le i \le 15)$ by putting the roots of generator polynomial G(x) into the received codeword polynomial R(x).

(4)



(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 4, April 2016

where,

$\mathbf{R}(\mathbf{x}) = \mathbf{r}_{254}\mathbf{x}^{254} + \mathbf{r}_{253}\mathbf{x}^{253} + \dots + \mathbf{r}_1\mathbf{x} + \mathbf{r}_0$		(5)
$G(x) = (x - \alpha^0) (x - \alpha^1) \dots (x - \alpha^{14}) (x - \alpha^{15})$		(6)
$S_{i} = R(\alpha^{i}) = r_{254}(\alpha^{i})^{254} + r_{253}(\alpha^{i})^{253} + \dots + r_{1}(\alpha^{i}) + r_{0}$	(7)	

If there is no error in the codeword, all the syndromes computed are zero. Non zero syndromes imply an error in the codeword and these are then passed to subsequent blocks for computing the error value and error location.

IV. THREE-PARALLEL SC BLOCK

As shown in fig. 2 [1], three parallel syndrome computation block for RS (255, 239) code is given. The received codeword consisting of 255 symbols which are multiple of 3, hence in syndrome computation block processing, reduction of clock cycles is targeted.

The received codeword from RS encoder which is to be given as input to the RS decoder is first stored in shift register and then inputted in parallel manner to the syndrome computation block. At the first clock, the received codeword($r_{254}, r_{253}, r_{252}$) are inputted parallel. As three symbols inputted parallel, first symbol r_{252} is going directly to the adder. Second symbolr₂₅₃ is first multiplied by α^i then added with r_{252} . Third symbol r_{254} is multiplied by $(\alpha^i)^2$ and then added with r_{253} and r_{252} . So the output of adder becomes $r_{254}(\alpha^i)^2 + r_{253}(\alpha^i)^1 + r_{252}$ which get stored in the latch (1). At the next clock cycle, the output of latch (1) is multiplied by $(\alpha^i)^3$, as three symbols are inputted and then added with $r_{251}(\alpha^i)^2 + r_{250}(\alpha^i)^1 + r_{249}$. This iterative process will be performed and syndromes S_i is stored in latch (1). Multiplexer selection (3) and (4) becomes 1 after all the clock cycle performed by one syndrome calculation, to shift syndromes S_i to the latch (2) and to compute new received codeword.



Figure.2 : Three parallel syndrome computation block

Three-parallel syndrome computation has to be done by equation (8). $S_{i} = R(\alpha^{i}) = ((...(r_{254}(\alpha^{i})^{2} + r_{253}(\alpha^{i})^{1} + r_{252})(\alpha^{i})^{3} + r_{251}(\alpha^{i})^{2} + r_{250}(\alpha^{i})^{1} + r_{249})(\alpha^{i})^{3} + ...)(\alpha^{i})^{3} + r_{2}(\alpha^{i})^{2} + r_{1}(\alpha^{i})^{1} + r_{0})$ (8)

Therefore the syndromes S_0, S_1, \ldots, S_{15} are outputted serially to the KES block.



Figure 3 : Serial connection of 16 syndrome blocks



(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 4, April 2016

The input patterns of the three-parallel syndrome computation cell are shown in Table 2. The whole computations will take 85 clock cycles as it is a three-parallel processing and we have total 255 symbols which are inputted in the group of 3.

Clock	1 st	2 nd	 85 th	1 th
Input A	r ₂₅₄	r ₂₅₁	 r ₂	r ₂₅₄
Input B	r ₂₅₃	r ₂₅₀	 \mathbf{r}_1	r ₂₅₃
Input C	r ₂₅₂	r ₂₄₉	 r ₀	r ₂₅₂

Table 2. Input patterns of the three-parallel syndrome computation cell

Fig. 4 shows the RTL schematic of SC block. And table 3 describes the complete processing of first SC block along with the inputs symbols and the clock cycles required for the process. Also output is shown.



Figure 4: Technology schematic of SC block

The iterative process will continued till all 255 symbols inputted parallel in the group of three as shown in table 3. Final output S1 is added with the next SC block and generates S2. Likewise the process continues till 16 syndrome values are computed. Fig. 5 shows the output waveform.

Step 1 Input symbols	Step 2	Step 3	Output
r ₂₅₄ , r ₂₅₃ , r ₂₅₂	$r_{254*}\alpha_{2,} r_{253*}\alpha_{1,} r_{252}$	output of step 2 * α_3	add to next 3 symbols
r ₂₅₁ , r ₂₅₀ , r ₂₄₉	$r_{251}*\alpha_{2,} r_{250}*\alpha_{1,} r_{249}$	output of step 2 * α_3	add to next 3 symbols
$r_{248}, r_{247}, r_{246}$	$r_{248}*\alpha_{2,} r_{247}*\alpha_{1,} r_{246}$	output of step 2 * α_3	add to next 3 symbols

Table	2.	Decocation	of	CC.	hladr
i adie	3:	Processing	OI	SC	DIOCK



(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 4, April 2016

r_{2}, r_{1}, r_{0}	$r_{2,r_{1,r_{0}}}$	r_{2}, r_{1}, r_{0}	Syndrome S1

Name Value		780 ns	Line	800 ns	L	820 ns		840 ns		860 ns	Linin	880 ns	Laura	900 ns		920 ns		940 ns		960 ns	(in al
▶ 📑 SYN[7:0] 11010110	110	11001	(11111)	10111	10101	00110	00001)	01011)	10111	00010	00000	01000	00010	11001	01101)	11101	11010.	11011)	(11001)	00110)	(10000)
▶ 📑 s0[7:0] 10101000	010	00110	01111	10011	11010	11100	00011)	10000)	01011	10111	00100	00101	00111	10101	00010)	00111	10101	01100)	01110)	10111)	(11101)
▶ 📑 \$1[7:0] 10101010	010	10101	11001	01110	10111	10110	11101	10111	01011	00001	01010	10110	10100	11000	01101)	10110	10101	11010	11101)	01010)	(10001)
▶ N 52[7:0] 11100000	011	10011	11010	00001	01010	10011	00010)	11010)	11010	10001	01110	00000	11011	00010	01101)	11100	11100	11000)	(10011)	11110)	(11100)
▶ N 53[7:0] 01110010	000	00000	10100)	10101	11101	10011	01111)	10010)	00111	10100	11110	11011	00100	11010	01000)	00001	01110	10011	(11111)	11000)	(10010)
▶ 🏹 s4[7:0] 00001101	100	11011	00101	11100	10111	01000)	(11110)	10011)	10000	00100	00110	01011	00100	10100)	10110)	11001	00001	11001	00100)	10111)	00111)
▶ 🍯 s5[7:0] 00100111	010	10101	(11111)	00101	10001	00000)	01010)	11101	01101	01010	00001	00001	10001	01111	01111)	01101	00100	01000)	(11011)	(11111)	00100)
▶ No 10110011	111	11011	01001	00010	01011	00111	01110)	00101	11101	00100	01101	11111	01011	01111	10111)	00100	10110	10110)	00011)	00101)	01100)
▶ 🍯 \$7[7:0] 00010000	101	10111	01010)	11100	00110	00101	(11111)	10101)	01110	01011	10111	00101	00010	11011	00010)	000	0000	00010)	01011)	01111)	01010)
▶ 📑 s8[7:0] 01010011	001	10010	11110	01000	11110	01000)	00101)	00101	00110	01000	01100	00100	10101	11000)	10110)	11111	01010	10110)	01111)	01100)	00001)
▶ 🃑 \$9[7:0] 11000001	110	10111	00011	11110	01011	00010	01011)	00010)	00011	11000	00110	01010	00010	00101	10100)	11100	11000	00011	01011)	10011	(10010)
▶ 📑 \$10[7:0] 00100100	111	11001	10111	01111	11100	10100	(10100)	10011)	11101	11100	11010	10110	00010	00101	00110)	00100	00100	01010)	(10010)	00100)	01101)
▶ 📑 s11[7:0] 00111110	100	11000	00110)	10001	00101	00000)	(11010)	01001)	00001	11111	01110	10101	11110	01000)	01101)	00000	00111	(11111)	(10000)	10111)	(11111)
▶ N 512[7:0] 11000101	100	01100	01100)	01100	01111	11011	(10101)	01110)	10001	10001	11001	01100	(11111	10111	01111)	00001	11000	01101	01111)	10100)	00111)
▶ N s13[7:0] 11000001	111	00110	(11010)	11100	10100	01110	(11000)	00111	01011	11110	10001	00110	100 10	10101	10110)	01110	11000	11011)	(11110)	10100)	(10001)
▶ N 514[7:0] 00110011	001	10100	(11100)	00010	11110	01000	01110)	11011	01111	01110	01111	00111	01111	01001	(11110)	10001	00110	01101	00010)	11001)	(11000)
▶ No[7:0] 10011001	001	10101	01000)	11011	10101	01100	01110)	11000)	01111	10111	01010	10111	01000	01001	10101)	01011	10011	11100)	00100)	100	00110
▶ N R1[7:0] 01111101	101	10110	00111	01011	00000	11101	01101)	01010)	00001	01111	10011	00111	11000	00010	10001)	00001	01111	11010)	(10100)	000	10111
▶ N R2[7:0] 10001110	010	00010	10101	11000	10011	00000)	01110)	11001	00101	00100	00110	11101	00110	10010	01000)	01011	10001	00111	01110)	101	11011
▶ Si[7:0] 0000000	-	-			_	-					000000	000							_	_	
👪 cik 🛛 1		-			1	<u> </u>								3	1						
😘 rst 🛛 0																		ļ			

Figure 5 : Simulation results which gives 16 syndrome values

V. RESULT AND CONCLUSION

The proposed syndrome computation block calculates the syndrome coefficients. As three symbols are inputted in parallel, the required clock cycles to compute each syndrome are reduced. Parallelizing allow inputs to be received at very high rates and outputs to be delivered at correspondingly high rates with minimum delay. As a result of this, speed of RS decoder will increase. The proposed three-parallel syndrome computation block has modeled in Verilog HDL and simulated to verify its functionality. For complexity consideration, constant-variable finite field multiplier is used to implement the syndrome computation block as it requires much fewer gates. The whole computation required 85 clock cycles. Table 4 shows results using Verilog HDL

Sr. No.	Parameters	Three-Parallel SC block
1	Clock Cycles	85
2	Delay	7.878ns
3	No. of gates	992
4	Technology used	Spartan 3

Table 4 Results	a neino	Veriloo	HDI
1 abic + i Coulta	s using	VUIIUE	

VI. REFERENCES

[1] Chang - Seok Choi and Hanho Lee, "High- Speed Low-Complexity Three parallel Reed-Solomon Decoder for 6 Gbps mm Wave WPAN Systems," *IEEE* Conference Publications, *Circuit Theory and Design*, pp.515-518, 2009.



(An ISO 3297: 2007 Certified Organization)

Vol. 5, Issue 4, April 2016

- [2] L. Song, M-L. Yu and M. S. Shaffer, "10 and 40- Gb/s Forward Error Correction Devices For Optical Communications," *IEEE Journal of Solid-State Circuits*, vol. 37, no. 11, pp. 1565-1573, Nov. 2002.
- [3] S. Lee, C-S. Choi and Hanho Lee, "Two-parallel Reed-Solomon base FEC Architecture for Optical Communications," *IEICE Electronic Express*, vol. 5, no.10, pp. 374-380, May 2008.
- [4] Hanho Lee, "High-Speed VLSI Architecture for Parallel Reed-Solomon Decoder," *IEEE Trans. on VLSI Systems*, vol. 11, no.2 pp. 288-294, April 2003
- [5] Jeong-In Park, Kihoon Lee, Chang-Seok Choi, and Hanho Lee, "High-Speed Low-Complexity Reed-Solomon Decoder using Pipelined Berlekamp-Massey Algorithm and Its Folded Architecture," *Journal of Semiconductor Technology and Science*, vol.10, no.3, september, 2010
- [6] Jae Do Lee and MyungHoonSunwoo, "Four Channel Three-Parallel Reed Solomon Decoder using S-DCME Algorithm for Optical Communications," *IEEE*, 2010.
- [7] S. Lee and Hanho Lee, "A High-Speed Pipelined Degree- Computationless Modified Euclidean Algorithm Architecture for Reed-Solomon Decoders", *IEICE Transactions on fundamentals of Electronics, Communications and Computer Sciences*, vol. E91-A no.3, pp.830-835, March2008.
- [8] Jeong-In Park, Jewong Yeon, Seung-Jun Yang, and Hanho Lee, "An Ultra High-Speed Time-Multiplexing Reed-Solomon-based FEC Architecture", *IEEE*, 2012.