

Optimized Router with Dynamic and low Latency Wireless NOC Architecture

R. Poovendran¹, S. Bill Clinton², R. Darshan³, R. Dinakar⁴, M. Fazil⁵

Assistant Professor, Department of ECE, Adhiyamaan College of Engineering, Hosur, Tamilnadu, India¹

U.G Student, Department of ECE, Adhiyamaan College of Engineering, Hosur, Tamilnadu, India^{2,3,4,5}

ABSTRACT: In this paper, we propose a novel wireless Network on Chip (NoC) design, which receives a virtual various higher topology to accomplish both low latency and power utilization. In this design, Radio Frequency Interconnect (RF-I) is utilized to build up the global network among switches. We likewise structure a dynamic transfer speed assignment plan to adjust the system traffic, and a productive multicast steering calculation to build the throughput. The results demonstrate that our design can accomplish at any rate 24.7% latency decrease and 17.8% power utilization sparing contrasted and wired Mesh Network on Chip. The efficient multicast routing algorithm to increase the throughput. The simulation results show that our architecture can achieve at least 24.7% latency reduction and 17.8% power consumption saving compared with wired Mesh NoC. The performance evaluation of a NoC can be also speeded up by doing it directly on hardware as FPGA instead of using simulation models.

KEYWORDS: Wireless Network, Latency and Power, Radio Frequency Interconnect, Throughput, Mesh

1. INTRODUCTION

Unpredictable topologies, for example Butterfly Fat Tree (BFT) and De Bruijn chart, are generally included by smaller network latency and lower power control utilization in Network-on chip configuration contrasted and Mesh topology [1]. Be that as it may, limited by the confused VLSI execution in customary CMOS circuit, they are not famous utilized in wired NoCs. Radio Frequency Interconnect (RF-I) [2], in which arbitrarily set switches encourage the usage of a wide range of topologies, offers a chance of irregular topologies. RF-I additionally gives valuable highlights, for example, communicating and adaptable transmission capacity allotment capacity for remote NoCs.

In our paper, we propose a virtual progressive topology for Wireless NoC (WNoC). RF-I is utilized to set up the remote association of sporadic topology on the worldwide dimension. We plan an ease dynamic data transmission distribution plan to reduce the traffic hotspots and increment throughput. We likewise propose an effective multicast steering calculation option in contrast to the Virtual Circuit Trees (VCTs) in wired NoCs by sending messages through devoted multicast channels. Contrasted with wired Mesh NoC with a similar scale, the proposed remote NoC engineering accomplishes both lower dormancy and power consumption.

With all routers accessing the wireless channel, we use Time Division Multiple Access (TDMA) + Frequency Division Multiple Access (FDMA) to divide the wireless channel into several sub-channels. FDMA is used to distinguish different frequency carriers. And TDMA is used to divide each carrier into several time slots; each one slot corresponds to a sub-channel. In this scenario, we get $N_c \sim N_1$ sub-channels from one electromagnetic waveguide, where N_c and N_1

indicate the amount of carriers of each electromagnetic waveguide and the number of sub-channels in one carrier, respectively. Since the capacity of the wireless channel is limited, we design a virtual hierarchical topology instead of a fully connected network. For the global level, routers are connected via a virtual and irregular topology of the wireless channel, which provides broadcasting ability and can dynamically adjust the

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Department of ECE, Adhiyamaan College of Engineering, Hosur, Tamilnadu, India

bandwidth of each link. For the local level, each router is connected to several Processing Elements (PEs) by wires. Figure 1 shows an example of a radix-L N-core wireless NoC.

WIRELESS NOC ARCHITECTURE

The work in this paper is partitioned in two phases. 1) Text-Detection 2) Inpainting. Content identification is finished by applying morphological open-close and close-open channels and joins the pictures. From there on, slope is connected to identify the edges pursued by thresholding and morphological widening, disintegration activity. At that point, associated part naming is performed to name each item independently. In Fig 1 it shows that, at long last, the arrangement of determination criteria is connected to sift through non content areas. After content discovery, content inpainting is practiced by utilizing model based Inpainting calculation.

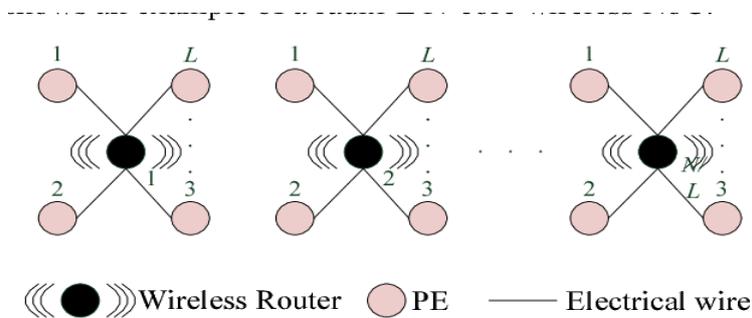


Figure 1. A schematic diagram of the hierarchical Wireless NoC.

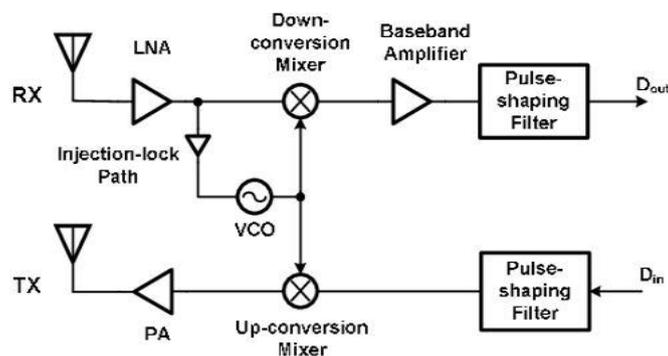


Figure 2. block diagram for transceiver mm WiNoC

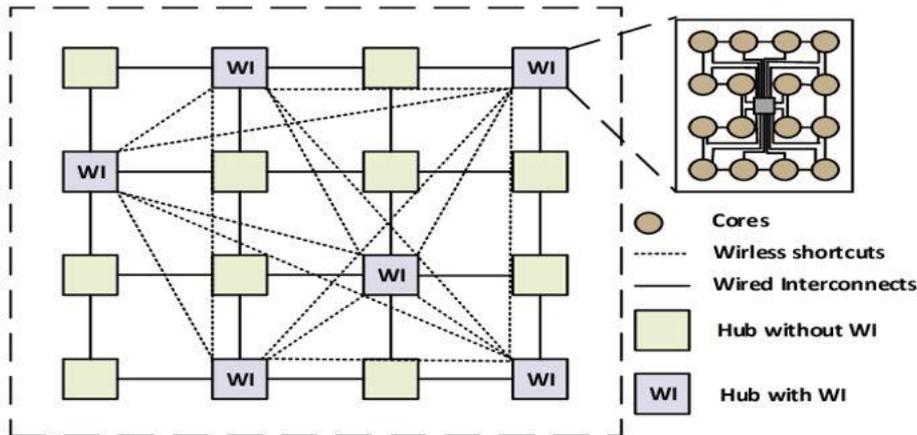


Figure 3. A Hybrid Hierarchical NoC Architecture

II. RELATED WORK

Unicast messages and multicast messages travel on devoted directs in WNoC engineering. The unicast channel is based on the virtual various leveled topology, and parcel transmission depends on the steering calculation of Clock Request R6_4 R2_10 R8_1 Authorization tack RA6_4 tackRA2_10 A6_4 tack R and ACKDatats6-D6_4 A case of multicast ask for approve process. Fig 2 and Fig 3 shows the process [8]-[11]. In this segment, we assess the execution of WNoC as far as power utilization and inertness. We use MSNS [3]framework to reenact the system inactivity. The recreation is executed under uniform and hotspot traffic designs [4].What's more, parcel infusion is displayed by two-state Marcov Modulated Process (MMP) [4]. The power utilization of the remote NoC originates from the switches, the electrical connections, and the RF-I interfaces, has explained in Fig 4. The Orion show [5] is utilized to give the dynamic and static power for the switches. For electrical connections, the power is given by (5).Unicast messages and multicast messages [12]-[15] travel on devoted diverts in WNoC design.

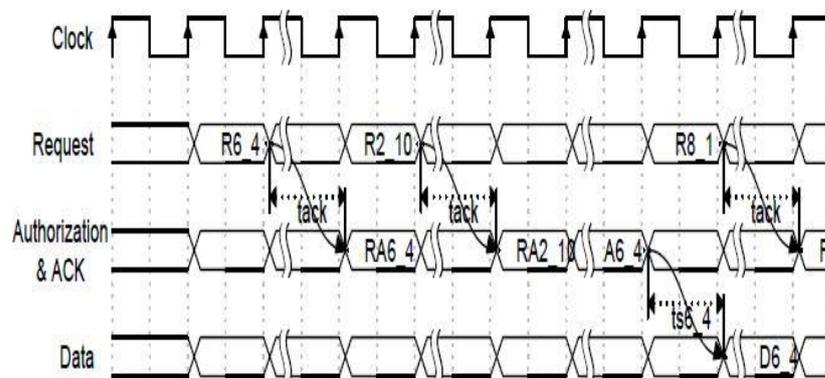


Figure 4. An example of multicast request-authorize process.

III. EXECUTION EVALUATION

In this segment, we assess the execution of WNoC as far as power utilization and inactivity. We use MSNS[3] framework to recreate the system idleness. The reenactment is executed under uniform and hotspot traffic designs [4]. Furthermore, bundle infusion is demonstrated by two-state Marcov Modulated Process (MMP) [4]. The power utilization of the remote NoC originates from the switches, the electrical connections, and the RF-I interfaces. The Orion show [5] is utilized to give the dynamic and static power for the switches, it is shown in Table 1. For electrical connections, the power is given by (5).

$$P_{link} = C_{cm, n} \cdot V_{DD}^2 \cdot f_{m, n}$$

$$P_{RF-I} = R_{m, n} \cdot I^2$$

where $C_{cm, n}$ and $f_{m, n}$ stand for the capacitance and bit

flipping recurrence of connection $R_{m, n}$, and V_{DD} is the voltage of the chip. For RF-I connects, the power devoured per bit (P_0) can be determined as (6).

Table 1: Hotspot traffic design

| System size | Sub-net size | No.of Sub-nets | Flat Mesh(nJ) | WiNoC(nJ) | NoC with G-Line(nJ) |
|-------------|--------------|----------------|---------------|-----------|---------------------|
| 128 | 16 | 8 | 1319 | 22.57 | 490.3 |
| 256 | 16 | 16 | 2936 | 24.02 | 734.5 |
| 512 | 16 | 32 | 4992 | 37.48 | 1012.8 |

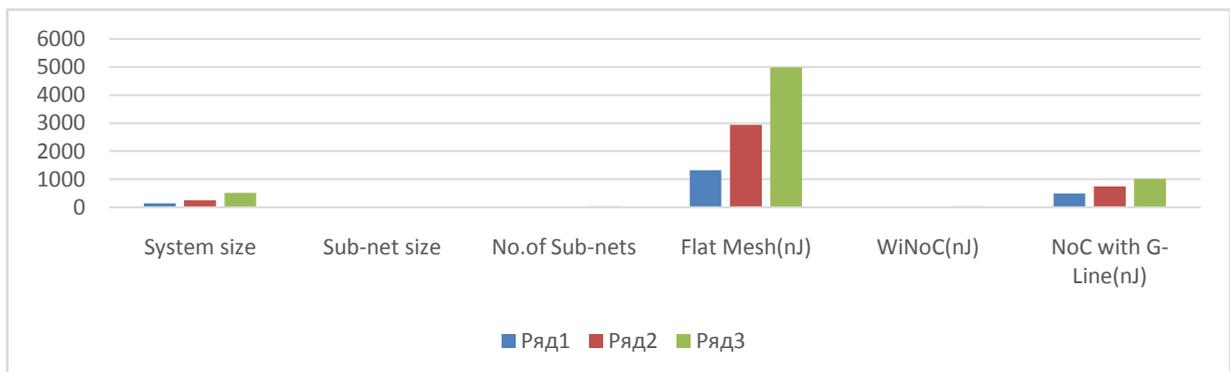


Figure 5: Hotspot traffic design graph

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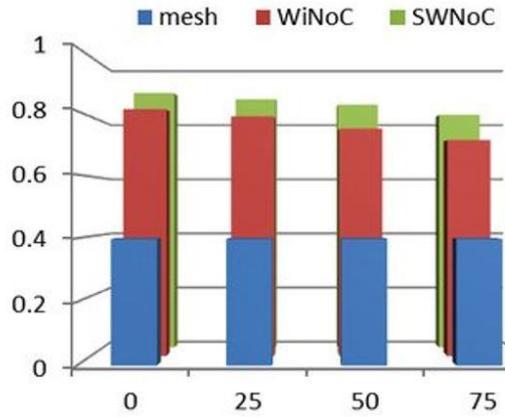


Figure 6: Percentage of wireless link

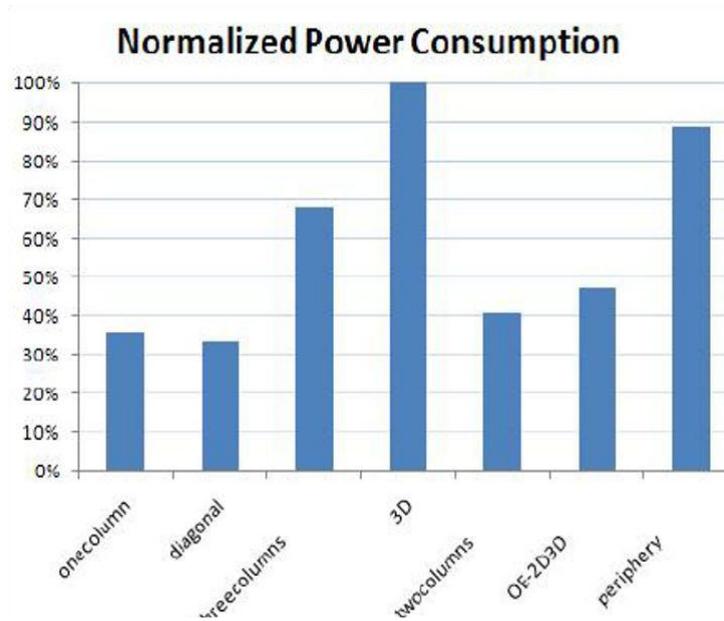


Figure 7: Power Consumption

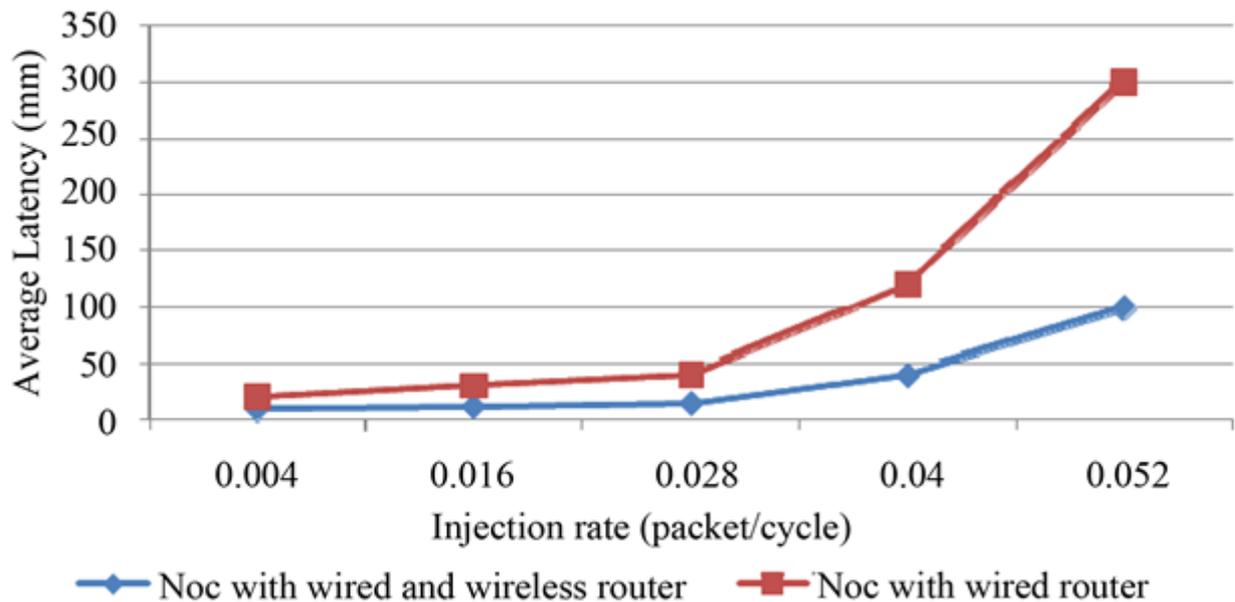


Figure 8: Average Latency NoC with wired,wireless and wired router

IV. CONCLUSION

In this paper, we propose a novel architecture for wireless NoC. By employing irregular topologies, the routing distance is largely reduced, so as to lower the network latency, shown in Fig 5 and 6. We design a low-cost dynamic bandwidth allocation scheme to balance the network traffic and increase the throughput. We also propose a simple and efficient multicast routing algorithm with the broadcasting advantage of RF-I. The results can accomplish at any rate 24.7% latency decrease and 17.8% power utilization sparing contrasted and wired Mesh Network, has shown in Fig 7 and 8. Simulation results prove that the proposed architecture can achieve both lower network latency and power consumption.

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Department of ECE, Adhiyamaan College of Engineering, Hosur, Tamilnadu, India

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