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Transformer Differential Protection of IEEE 14 Bus Using DigSilent

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ABSTRACT: This paper analyses a study on IEEE 14 bus power system using DigSilent software. The simulation of load flow study is performed using IEEE 14 bus system. The simulation of short circuit study is performed for the IEEE 14 bus system. The different protection schemes are studied for the given power system network. Further the simulation is carried out for transformer differential protection scheme using DigSilent software for the IEEE 14 bus system.

KEYWORDS:Load flow analysis, transformer differential protection, DigSilent, SEL487E

I. INTRODUCTION

A load-flow study is a steady-state analysis whose target is to determine the voltages, currents, and real and reactive power flows in a system under a given load conditions. Power flow analysis is very important in planning stages of new networks or addition to existing ones like adding new generator sites, meeting increase load demand and locating new transmission sites. The load flow solution gives the nodal voltages and phase angles and hence the power injection at all the buses and power flows through interconnecting power channels. It is helpful in determining the best location as well as optimal capacity of proposed generating station, substation and new lines. It determines the voltage of the buses. The voltage level at the certain buses must be kept within the closed tolerances. System transmission loss minimizes. Economic system operation with respect to fuel cost to generate all the power needed The line flows can be known. The line should not be overloaded, it means, we should not operate the close to their stability or thermal limits. A short circuit is an abnormal condition of relatively low resistance between two points of differing optional in a circuit, usually resulting in a flow of excess current. The purpose of the short circuit study is to determine the fault current available at the various buses on your system. These are worst-case single and three phase bolted faults. With this information you can determine if your equipment is sized appropriately and will not fail during a fault event. The waveform-based approach that enhances the differential protection dependability by detecting the geomagnetically induced current and overriding the Harmonic blocking signals for the internal faults by recognizing the waveforms are all asymmetric in one direction [1]. The novel method to discriminate between the magnetizing inrush and external and internal fault currents in power transformers. It is executed by using fuzzy expert system [2]. In case of a three-phase short circuit, stability of a power transformer differential protection's operation is higher than in case of one-phase short circuits. Hence real time simulation is required for effective utilization[3-4]. The trip time taken is (50 micro sec) is also acceptable in order to ensure that the algorithm will give a proper decision to discriminate between a fault current and an inrush current. The relay is restrained in all the cases for the inrush current, normal load current or the external fault current. The simulation is done using MATLAB Simulink environment. The work can be improved when it is simulated using real time software [6]. The protection based on combined S transform (DST) and SVM algorithm provides a differential protection for Power Transformer which provides much more security than the other existing DWT, ANN. The experimental results can be executed for better enhancement of results [7-11]. Paper is organized as follows. Section II describes about DigSilent software. The implementation of transformer differential technique using the DigSilent software is given in Section III. Section IV presents simulation results. Finally, Section V presents conclusion.

II. DIGSILENT SOFTWARE

A short circuit study is used to determine the worst-case current scenario during a three phase and single phase bolted fault. This is critical design information when selecting equipment for a new design, and replacing existing equipment during upgrades. Two factors contributing to the fault current is the size and impedance of the transformer supplying the circuit, and the available fault current that can supplied by the utility, and on-site generation. These may be due to

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internal or external effects. Internal effects include breakdown of transmission lines or equipment, aging of insulation, deterioration of insulation in electrical equipment. External effects include overloading of equipment, insulation failure due to lighting surges and mechanical damage by public. Arcing faults can lead to fire and explosion in equipment such as transformers and circuit breakers. Abnormal currents cause the equipment to get overheated, which further leads to reduction of life span of their insulation. The operating voltages of the system can go below or above their acceptance values that creates harmful effect to the service rendered by the power system. TheDigSilent is the preferred high-end tool for solving problems related to transmission distribution and industrial power systems. It is easy to use, fully Windows compatible and combines reliable and flexible modelling capabilities with state-of-the-art algorithms and unique database concepts. DigSilent is a leading power system analysis software application for use in analysing generation, transmission, distribution and industrial systems. It covers the full range of functionality from standard features to highly sophisticated and advanced applications including wind power, distributed generation, real-time simulation and performance monitoring for system testing and supervision. DigSilent fully covers your daily needs including high-end applications in new technologies such as wind power and distributed generation. Besides the standalone functionality, the DigSilent software engine integrates smoothly into GIS, DSM or EMS supporting open system standards. DigSilent is the most economical solution as data handling, modelling capabilities and overall functionality replaces a set of other software systems minimizing project execution costs and training requirements. The all-in-one DigSilent solution is optimizing your workflow aspects in the best possible way. The transformer is one of the major equipment in power system. It is a static device, totally enclosed and usually oil immersed, and therefore the fault occurs on them are usually rare. But the effect of even a rare fault may be very serious for a power transformer. Hence the protection of power transformer against possible fault is very important. The fault occurs on the transformer is mainly divided into two type external faults and internal fault. External fault is cleared by the relay system outside the transformer within the shortest possible time in order to avoid any danger to the transformer due to these faults. The protection for internal fault in such type of transformer is to be provided by using differential protection system.



Fig 1.IEEE 14 bus power system

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III. TRANSFORMER DIFFERENTIAL PROTECTION

The working principle of Differential Protection System explained as follows. Normally, the operating coil carries no current as the current are balanced on both the side of the power transformers. When the internal fault occurs in the power transformer windings the balanced is disturbed and the operating coils of the differential relay carry current corresponding to the difference of the current among the two sides of the transformers. Thus, the relay trips the main circuit breakers on both sides of the power transformers is shown in figure 1. The protection for internal fault for the transformer is to be provided by using differential protection system. The steps are followed in the power factory software. Draw the diagram, enter the data and perform the load flow for the test system. Include the circuit breaker, configure the current transformer settings and relay settings. Perform load flow and short circuit in internal zone and external zone. This seminar gives eye opener for the protection in the electrical power system. The three bus test system and the output is shown in figure below. The procedure for implementing transformer differential protection relay is as follows. Right click in transformer to be protected and choose disconnect element. Choose junction and put it near to both side of the transformer. Right click in the transformer and choose connect element. Select circuit breaker and connect it into bus bar. Close circuit breaker by double click it. For setting CT (current transformer): Right click new device, current transformer, name it as CT. Go to type, new project, name it as CT, put primary equal to 100A and secondary equal to 5 A. To change the location: Go to cubicle, choose the bus bar, go to each switch, choose the switch which contains circuit breaker, and Go to arrow double click then ok. File- import-SEL487Eactivate the project-check the relay library. For setting a relay: Right click the small box (cubicle), new device, relay model, relay time, select project, go to + sign of relay library, + sign of Schweitzer, + sing of SEL 487E, choose SEI 487E 5A then ok. Next double clicks the buses that near to the transformers to see the voltage how much. Right click the small box (cubicle), edit devices, double click relay model, and make all the components in net element out of service except (S measurement, T measurement, differential and logic). Right click the small box (cubicle), edit device, double click relay model, go to create CT for second winding of transformers, name it as CT, put primary equal to 100A and secondary equal to 5A. To change the location: Go to cubicle, choose the bus bar, Go to each switch, choose the switch which contains circuit breaker, and Go to arrow double click then ok. Go to S measurement double click, go to edit Adapter double click enter the value of voltage. Go to T measurement double click, go to Edit Adapter double click enter the value of voltage, ok, close. Double click junction enters the bus voltage, then ok. Perform the load flow calculation. Right click the junction, calculate, short circuit, execute. Right click the cubicle, show, differential protection plot. See the tripping time when create fault (short circuit) in bus bar and near to the transformer.



Fig 2. Differential protection for power transformer

IV. EXPERIMENTAL RESULTS

The simulation of differential protection is exhibited using matlab Simulink environment using phase difference algorithm, relay logic and wavelet algorithm is discussed in the literature [12-14]. The IEEE 14 bus system is drawn in the graphic window and the data are entered in the DigSilent software and is shown in figure 3.

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Fig 3.IEEE 14 bus in DigSilent Software

The load flow is executed by using the DigSilent software and the convergence is achieved in five iterations as shown in figure 4.

DDIgBl/info = Element 1 @ Symphronous Machine (2)' is local reference in separated avec of 1 == 11	
Dig31/info - Element ' @ Synchronous Machine' is local reference in separated area of ' - 1'	
Diggi/Anto - Grid split into 2 isolated areas	
Dig31/info -	
DIgSI/info - Start Newton-Repheron Algorithm	
Digit/info - load flow interation: 1	
Digitinio - iosi fice isentini 2 Digitinio - iosi fice isentini 3	
DIgSI/info - load flow iteration: 4	
DIgSI/info - load flow iteration: 5	
DigBI/info - Newton-Rephano converged with 5 iterations.	
Digitinfo -	
DIg31/info - Report of Control Condition for Relevant Controllers	
DIg51/1650 =	
Digit/info - 'oria'Synchronous Hemine.EleSys'i	
DidSiverg - * Synchronous Machine' : Maximum Active Power Limit exceeded (P = 24.40 MM > Tmax = 0.00 MM)	
DIgSI/info -	
Digilest Project:	
15.2.6 Date: 3/19/2018	1
I Load Flow Calculation Complete System Report: Substations, Voltage Frofiles, Grid In	sterchange 1
i Mr. Load Flow, balanced consistive semisories i Automatic Model Adaptation for Convergence	No
Automatic Tap Adjust of Transformers No Max. Acceptable Load Flow Error for	
1 Consider Reactive Power Limits No 1 Nodes	1.00 KVA I
1 Nodel Equations	0.10 1 1
I Grid: Grid System Stage: Grid I Study Case I Annex:	/1 /
rated Active Reactive Power	1
Voltage Bus-voltage Power Fower Factor Current Loading Additional Data	1
I (KV) (p.u.) (KV) (deg) (H9() (Hver) (-) (KA) (4) I	
iSingle Bushar(1)	
88 132.00 0.98 129.97 ~26.90	i i
I Cub_1 /Lod General Load 14.00 6.00 0.92 0.07 IP101 14.00 MW Q101 6.00 Mvar	1
I Cub_1 /Lne Line(10) -0.13 1.79 -0.07 0.01 0.00 (Pv: 5.67 kW cLod: 0.00 Hvar L:	1.00 km/
Cub_1 /Lne Line(12) -1.23 -0.94 -0.79 0.01 0.69 (Pv: 5.49 kW cLod: 0.00 Mvar L:	1.00 km1
1 CUD_1 /Ane Line(13) -12.64 +6.05 +0.00 0.06 6.39 (PV) 140.98 kW CLodi 0.00 MVar Li	1,00 831

Fig 4.Convergernce of IEEE 14 bus using DigSilent Software

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The active and reactive power, current and percentage of loading for the IEEE 14 bus is obtained as shown in figure 5.

1		1						1.	DIGSILEN	I Proje	1051			_	
								- 17	15.2.6	Dates	3/19/2	2018			

		*****************	********		******	*******	*******	*****				******	******		
Load Flo	ew Calcul	ation			Comple	te Syste	n Report	1 Subs	tations,	Voltage Pro	files, (brid In	terchan	ige	
AC 1	Load Flow	, balanced, positive	sequence		1	Automa	tic Mode	1 Adap	tation for	Converger	.ce		No		
Automatic Tap Adjust of Transformers					No Max. Acceptable Load Flow Error for						10.7				
Consider Reactive Power Limits					No I Nodes							1.00 kVA			
1					Model Equations								0.10 %		
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i vridi vi		System Sta	del otro		1 210	ay case:	study c	******		Annes				<u>.</u>	
1	rated		Active	Reactive	Power			1							
1	Voltage	Bus-voltage	Power	Power	Factor	Current	Loading	1		Addition	al Data				
1	[kV]	[p.u.] [kV] [deg] [304]	[Hvar]	[-]	[kA]	[4]	1							
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Single B	usbar(1)	12/12/07 12/27 12/27 12/27/2	2					1							
1 88	132.00	0.98 129.97 -26.9	9					1							
Cub_1	/Lod	General Load	14.00	6.00	0.92	0.07		19101	14.00 1	Q101	6.00 10	AT			
Cub_1	/Lne	Line(10)	-0.13	1.79	=0.07	0.01	0.80	1 PVI	5.67	eLod:	0.00 10	ar L:	1.00	1 8	
Cub_1	/Lne	Line(12)	-1.23	-0,94	-0.79	0.01	0.69	1 PV1	5.49	W clodi	0.00 M	AF LI	1.00	1 8	
Cub_1	1204	PTD6 (13)	-24.04	-6.05	-0.00	0.00	41.39	1001	740.90	CM 02001	0.00 10	10E 101	4.00	1. 4	
Sinale B	ushar (10)														
BB	220,00	1.02 223.63 -26.7													
Cub 1	/Lne	Line	6.84	-6.92	0.70	0.03	2.51	(PV)	17.76	W cLodi	0.00 M	AF LI	1.00	5 10	
Cub_1	/Lne	Line(1)	-6.84	6.92	-0.70	0.03	2.51	I PVI	49.51	W cLod:	0.00 M	AT LI	1.00	k	
Cub_1	/Tr2	2-Winding Transfor	0.00	0.00	1.00	0.00	0.00	Tap:	0.00	Mint	0	Max	1 0	2	
Single B	usbar(11)														
1 88	132.00	1.00 131.50 -25.9	7					1							
Cub 1	/Lod	General Load(7)	4.00	2.00	0.89	0.02		P10:	4.00 1	Q10:	2.00 M	ar.			
Cub_1	/Lne	Line(14)	2.33	-3.39	0.57	0.02	1.01	I Pv:	-0.00	W cLod:	0.00 10	ar L:	1.00) k	
Cub_1	/Loe	Line(9)	-6.33	1.39	-0.98	0.03	2,85	I PVI	34.72	eLod:	0.00 M	er Li	1.00) ki	
Single Bo	usbar(12)														
0.00	220.00	1.01 221.12 -25.7	2					1							
Cub_1	/Lod	General Load(9)	8.00	2.00	0.97	0.02		P10:	8,00 1	Q10:	2.00 M	AT.			
Cub_1	/Lne	Line(1)	6.89	-6.72	0.72	0.03	2.51	1941	49.51	eLods W	0.00 M	er Li	1.00	2 4	
Cub_1	/Lne	Line(2)	10.30	-11.36	0.67	0.04	4.00	1 PVI	132.55	W cLods	0.00 M	AF LI	1.00) k	
Cub_1	/Loe	Line(4)	-53.93	6.38	-0.99	0.14	14.10	Pv :	309.79	W cLod:	0.00 M	'ar Li	1.00) k	
Cub_1	/Tr2	2-Winding Transfor	28,75	9.69	0.95	0.08	30.18	ITap:	0.00	Mint	0	Max	t 0	2	

Fig 5.Results of bus data in IEEE 14 bus using DigSilent Software

The per unit voltage for the IEEE 14 bus is obtained with deviation percentage as shown in figure 6.

I Load Flow Calcula	tion				Comple	te Syster	Repor	ti Pub	stations, V	oltage Profiles,	Grid Interchange		
AC Load Flow, balanced, positive sequence Automatic Tap Adjust of Transformers Consider Reactive Power Limits					i Automatic Model Adaptation for Convergence No i Max. Acceptable Load Flow Error for Nodes i Model Equations						No 1.00 kVA 0.10 %		
Grid: Grid	System Stage: Grid				i Sta	dy Case:	Study	Case		i Annex:	/ 5		
1	rtd.V [kV]	Bus [p.u.]	- voltage [kV] [deg]			-10		-5	Voltege -	Deviation (%)	+10		
BB	132.00	0.985	129.97 -26.90						-				
BB BB	220.00	1.016	223.63 -26.78						-				
BB	132.00	0.996	131.50 -25.97										
BD Busbar(12)	220.00	1.005	221.12 -25.72										
BB BB	220.00	1.010	222.17 -24.39						-				
BB	220.00	0.985	216.61 -30.75										
BB Single Bushar(16)	13.00	0.985	13,60 -30,75						-				
BB Single Bushar(17)	132.00	1.087	143.55 -0.33										
BB Single Busbar(15)	13.00	1.090	15.04 0.00						-				
BB Single Busbar(2)	132.00	0.979	129.17 -26.69										
BB Single Busbar(3)	132.00	0.989	130.59 -26.88										
BB Single Busbar(4)	220.00	1.019	224.23 -27.08						8				
BB Single Busbar(5)	13.80	1.020	14.08 -27.01										
BB Single Busbar(6)	132.00	0.999	131.85 -25.21						1				
BD Single Busbar(7)	13.00	1.003	13.84 -26.21						- E				
88	132.00	1.008	133.03 -24.55										

Fig 6.Results of per unit voltage of bus in IEEE 14 bus using DigSilent Software

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It is observed that when the three phase symmetrical fault occurs at outside the zone of transformer, the differential relay does not activate and it shows the tripping time 99999 seconds is shown in figure 7.



Figure 7 outside zone of transformer differential protection

When the three phase symmetrical fault occurs at inside the zone of transformer, the differential is activated and the tripping time is 0.015 seconds as shown in figure 8.





V. CONCLUSION

This paper successfully completed the load flow and short circuit study for IEEE 14 bus power system network. The load flow study is simulated using DigSilent software by using Newton Raphson method. The result is converged in 5 iterations. The transformer differential protection is implemented using SEL487E relay model in the DigSilent software. It is observed that when three phase short circuit fault occurs near the junction of transformer terminals the differential relay activates in the transformer zone only. When three phase fault occurs at other than transformer zone the relay does not activate. In future this study can be implemented by using overcurrent protection for IEEE 14 bus system.



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BIOGRAPHY



Dr.N.Murali has 20 years of teaching experience. Working as a Lecturer in the Department of Electrical Engineering, University of Technology and Applied Sciences Nizwa, Oman. His current areas of research are Optimization techniques, power electronics and drives, electrical machines design using Ansys, power electronics application in power system, IOT based renewable energy systems and power electronics application in renewable energy systems. He has published 19 research papers in

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