



Lag Compensators from Universal Design Method

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ABSTRACT: Compensators are corrective sub systems to force the chosen plant to meet the given specifications. Their purpose is to compensate for the deficiency in the performance of the plant. The Universal chart facilitates accurate compensators design and also satisfies the system specifications in frequency domain and steady state error. This paper shows the derivation of lag compensator from universal design which is useful for different time domains. The design is carried out with the approach of frequency domain specification.

KEYWORDS: Lag compensators, Universal design, Universal design chart, Bode plots, Continuous systems.

I. INTRODUCTION

Control system design via the frequency domain and specifically using the Bode plots has been well established. Various Bode design charts and formulae were developed for the most common continuous-time and discrete-time compensators by Yeung. This paper enhances, emphasis and shows the possibility to derive lag compensators from a single universal design which also facilitates the design of many of other conventional compensators. In the sequel, first the essence of the method is presented, and then formulae for the compensators are given which are used in conjunction with the universal design.

II. DESIGN METHOD PRINCIPLE

To illustrate the basic idea of the compensator design, consider a plant with the frequency response $G_p(j\omega)$. A compensator $G_c(j\omega)$ is to be inserted in series with the plant so that a desired value for the phase margin (PM) or gain margin (GM) is obtained.

By the definitions of PM and GM, for the case of PM

$$|G_{c}(j\omega_{g})G_{p}(j\omega_{g})|_{db} = 0$$

$$\angle (G_{c}(j\omega_{g})G_{p}(j\omega_{g})) = PM - 180^{\circ}$$
(1)

and for the case of GM

$$|G_{c}(j\omega_{ph})G_{p}(j\omega_{ph})|_{db} = -GM$$

$$\angle (G_{c}(j\omega_{ph})G_{p}(j\omega_{ph})) = -180^{\circ}$$
(2)

Where $|\ |_{db}$ denotes the 20log decibels of the magnitude, $|\ |_{d}$ denotes the phase angle. ω_g is the gain crossover frequency and ω_{ph} is the phase crossover frequency.

Equations (1) and (2), respectively, can be rewritten as

$$\begin{split} |G_c(j\omega_g)|_{db} &= -|G_p(j\omega_g)|_{db} \\ & \angle (G_c(j\omega_g) = - \angle G_p(j\omega_g) + PM - 180^{\circ} \\ &\text{and} \end{split} \tag{3}$$

$$\begin{split} |G_c(j\omega_{ph})|db &= -|G_p(j\omega_{ph})|_{db} = -GM\\ & \angle \left(G_c(j\omega_{ph}) = -\angle G_p(j\omega_{ph}) - 180^o \right) \end{split} \tag{4}$$

The left-hand sides of (3) and (4) depend only on the compensator. It can also be shown that for all the compensators frequency response can be normalized into the standard form.

$$G_{c}(A, B) = \frac{(1+jB)}{}$$

$$(5)$$

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$$(1+iA)$$

where A, B and C are in general functions of the frequency and compensator parameters. Let

$$G_c(A, B) = \frac{(1+jB)}{(1+jA)}$$
 (6)

so that

$$G_{c}(j\omega) = CG_{c}(A, B) \tag{7}$$

Substitution of (7) into (3) at $\omega = \omega_g$ gives

$$|G_{c}(A, B)|_{db} = -|CG_{p}(j\omega)|_{db}$$

 $\angle G_{c}(A, B) = -\angle (CG_{p}(j\omega)) + PM - 180^{\circ}$ (8)

Substitution of (7) into (4) at $\omega = \omega_{ph}$ gives

$$\begin{aligned} |G_c(A, B)|_{db} &= -|CG_p(j\omega)|_{db} - GM \\ &\angle G_c(A, B) = -\angle (CG_p(j\omega)) - 180^{\circ} \end{aligned} \tag{9}$$

If C is known, then the right-hand sides of (8) or (9) can be plotted as a plant curve. Intersection of plant curve with the appropriate curve on the universal design chart will yield the A and B values which in turn will give the compensator parameter values.

III. PLOTTING OF UNIVERSAL DESIGN CHART

Standard form of compensator in frequency domain is

$$G_c(A, B) = \frac{(1+jB)}{(1+jA)}$$

The above equation in rectangular form is given as

$$G_{c}(j\omega) = C \frac{(1+jB)}{(1+jA)}$$

$$= C \frac{(1+AB)}{(1+A^{2})} + jC \frac{(B-A)}{(1+A^{2})}$$
The platting of universal design

This is the basic equation to plot the universal design chart. The plotting of universal design chart is done in two steps.

- 1. Plotting of the A curves.
- 2. Plotting of the B curves.

PLOTTING OF A CURVES:

A curves are plotted using basic equation by varying the values of A for the fixed value of B. The A curve is drawn with the magnitude on Y-axis and the phase angle on X- axis. This curve is known as A curve. To draw more A curves above procedure can be repeated for different values of A.

PLOTTING OF B CURVES:

B curves are plotted using basic equation by varying the values of B for the fixed value of A. The magnitude is taken on Y- axis and the phase angle is taken on X- axis. This curve is known as B curve. Many values of A gives many B curves. To draw more B curves above procedure can be repeated for different B values.

The universal design indicates the plotting of A and B curves on same plane which results in universal design chart.

IV. TRANSFORMATION OF PHASE- LAG COMPENSATOR INTO STANDARD FORM

The frequency response of the phase-lag compensator is equated to the standard form

$$G_{c}(j\omega) = C \frac{(1+jB)}{(1+jA)} = K \frac{(1+jT\omega)}{(1+j\beta T\omega)}$$
(10)

yielding

$$C = K \tag{11}$$

$$\beta = A/B \tag{12}$$

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$$T = \beta/\omega \tag{13}$$

Frequency is to be specified as either ω_g or ω_{ph} .

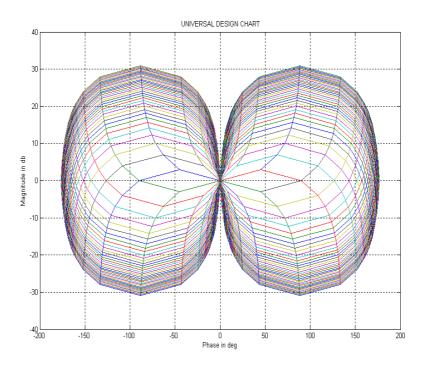


Fig. 1 General Universal Design Chart

V. ILLUSTRATIVE EXAMPLE

Following illustrates the designing of phase- lag compensator to satisfy the following specifications:

- (i) The phase margin of the system = 48 degree (ii) Velocity error constant $K_v = 2$
- (iii) The gain crossover frequency of the system must be less than 2.5 rad/sec for the system $G_p(S) = 3 / S (S^2 + 4 S + 5), H(S) = 1.$

Method: The given plant transfer function is

$$G_p(S) = \frac{3}{S(S^2 + 4 S + 5)}$$

The transfer function of phase lag compensator is

$$G_c(S) = \frac{K (1+S T)}{(1+S\beta T)}$$

Transformation of compensator into standard form yields

$$C=K$$
 (K is determined from steady state requirements) $\beta=A$ / B $T=\beta$ / ω

From the data, $K_v = 2$ and

$$\begin{array}{ccc} & & Lt & \\ K_v & = & & S \; (G_p(S) \; G_c(S)) \\ & S \! \rightarrow \! 0 & & & \\ Lt & 3 & & K(1+S \; T \;) \end{array}$$

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$$K_v = S$$
 = 2
 $S \rightarrow 0$ $S(S^2 + 4S + 5)$ $(1 + S\beta T)$ = 2

Therefore
$$K = 3.33$$
 and $C = 3.33$
So

$$\begin{split} G_p(S) &= 3/S(S^2 + 4S + 5) \\ G_p(jw) &= -12/(25 + 6\ \omega^2 + \omega^4) - J\ (15\ /\ (\omega - 3\omega))\ /\ (25 + 6\omega^2 + \omega^4) \\ X &= -12\ /\ (25 + 6\ \omega^2 + \omega^4) \\ Y &= (15\ /\ (\omega - 3\omega))\ /\ (25 + 6\omega^2 + \omega^4) \\ Z &= X\ - j\ Y \\ |G_p(j\omega)|\ db &= 20\ log(abs(Z)) \\ |G_c(j\omega)|\ db &= |C\ G_p(j\omega)|\ db \\ \angle\ G_p(j\omega) &= -90\ - tan^{-1}(4\omega/(5 - \omega^2)) = -90\ - (180\ - tan^{-1}(4\omega/(5 - \omega^2))) \\ \angle\ G_c(A,B) &= -\ \angle\ CG_p(j\omega) + PM\ - 180 \\ &= -(-90\ - (tan^{-1}(4\omega\ /\ (5 - \omega^2)))) + PM\ - 180 \\ &= 90\ + (180\ + tan^{-1}(4\omega\ /\ (5 - \omega^2))) + 132 \end{split}$$

Using above equations plant curve is drawn on universal chart.

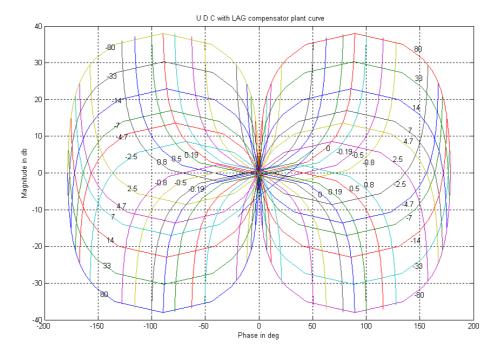


Fig. 2 Universal design chart with lag compensator plant curve

From above figure A, B values are 0.19 and 2.5. Therefore

$$\beta = A / B = 0.19 / 2.5 = 0.0760$$

$$T = B / \omega = 2.5 / 2.5 = 1$$

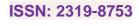
With these β and T values compensator transfer function is

$$G_c(S) = \frac{3.33(1+S)}{(1+0.0766 S)}$$

Therefore the compensated system becomes

$$G_p(S)G_c(S) = \frac{3}{S(S^2 + 4S + 5)} \ \frac{3.33(1 + S)}{(1 + 0.0.076 \ S)}$$

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VI. CONCLUSIONS

This paper provides design of lag compensators using a universal chart which enables an efficient design of the most common compensators also. A method for lead compensator design is presented. It is based on the idea of normalizing compensator parameters so that a design chart can be generated once for all. A design example is carried through to illustrate the use of this design chart. The coding is done in MATLAB. The method proves to take less time and iterations compared to conventional compensator design. The method involves less complexity. Simultaneous fulfillment of the specifications of steady-state error, phase margin, gain margin, and gain crossover frequency can be achieved with accuracy.

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REFERENCES

- [1] Kai Shing Yeung, Kuo Hsum Lee, "A Universal Design Chart for Linear Time-Invariant Continuous-Time and Discrete-Time Compensators" IEEE Trans. on Educ., Vol.43, No.3, pp.309-315, Aug. 2000.
- [2] R. Kiranmayi, "Lead Compensators from Universal Design Method" IJAREEIE, Vol. 1, Issue 3, 2012.
- [3] R. Kiranmayi, "Observation of System Performance for Lead Compensation from Universal Design Method" IJAREEIE, Vol. 1, Issue 4, 2012.
- [4] W.R.Wakel, "Bode compensator design," IEEE Trans. Automat. Contr., Vol. AC-21, pp.771-773, Oct. 1976.
- [5] J.R.Mitchell, "Comments on bode compensator design," IEEE Trans. Automat. Contri., vol. AC-22, pp.869-870, Oct.1977.
- [6] K.Q.Chaid, "Bode compensator design," M.S. Thesis, Univ. Taxes, Arlingtion 1986.
- [7] K.S.Yeung, K.Q.Chaid, "Bode design formulas for discrete compensators," Electron. Lett., vol.22, no.24, Nov. 21, 1986.
- [8] K.S.Yeung, K.Q.Chaid, and T.X.Dinh, "Bode design chart for continuous-time and discrete-time compensators" IEEE Trans. Educ., vol.38, pp252-257, Aug.1995.
- [9] K.S. Yeung, K.W. Wong, and K.L. Chen "A nontrial-and-error method for lag-lead compensator design" IEEE Trans. Educ., vol.41, pp.76-80, Feb. 1998.
- [10] Ogata K., "Modern control Engineering" Practice Hall of India, Pvt. Ltd., New Delhi, 1995.
- [11] D.R.Wilson, "Modern Practice in servo design" Pergmon Press, 1970.
- [12] Benjamin C.Kuo, "Automatic control systems" Practice Hall of India, Pvt. Ltd ., New Delhi, 1979.

BIOGRAPHY



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